

EAST - [default.wsp:1]

File View Edit Tools Window Help

Drafts
Pending
Active
L1: (5) ddr same chip same strobe same (latch\$3 or bufi
L2: (36) (ddr same chip) and strobe same (latch\$3 or bu
L3: (259) (ddr same chip) and strobe and (latch\$3 or bu
Failed
Saved
Favorites
Tagged (6)

DBs: USPAT; US-PGPUB; EPO; JPO
Default operator: OR
Highlight all hit terms initially

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	5	ddr same chip same strobe same (latch\$3 or buffer\$3) same control\$3 same delay\$3	USPAT; US-PGPUB; EPO; JPO
2	BRS	L2	36	(ddr same chip) and strobe same (latch\$3 or buffer\$3) same control\$3 same delay\$3	USPAT; US-PGPUB; EPO; JPO
3	BRS	L3	259	(ddr same chip) and strobe and (latch\$3 or buffer\$3) and control\$3 and delay\$3	USPAT; US-PGPUB; EPO; JPO

Start In... E... C... C... ht... Bi... D... E... E... E... 10:07